

CLAIMS

Claims 1-24 (cancelled).

25. (previously presented) A radio receiver, comprising: a band pass filter configured to filter a combined signal; a clocked comparator coupled with the band pass filter, the clocked comparator configured to compare the filter combined signal to a ground reference when the comparator is enabled by a clock signal; a digital-to-analog converter coupled with the clocked comparator, the digital-to-analog converter configured to convert the output of the clocked comparator to an analog signal; and a combiner configured to receive a RF signal and combine the RF signal with the analog signal generated by the digital-to-analog converter in order to generate the combined signal; and a plurality of clocked comparators coupled to the band pass filter, each of the clocked comparators configured to be activated on a different phase of a clock signal and a combiner coupled to the plurality of clocked comparators, the combiner configured to combine the outputs of the clocked comparators.

26. (original) The radio receiver of claim 25, further comprising filtering and decimation circuitry configured to filter and decimate the output of the clocked comparator.

27. (original) The radio receiver of claim 25, further comprising a clock signal configured to clock the clocked comparator at a rate required to achieve a selected effective number of bits at the output of the filtering and decimation circuitry.

28. (original) The radio receiver of claim 25, wherein the combiner is a passive combiner.

29. (canceled).

30. (previously presented) The radio receiver of claim 25, wherein the digital-to-analog converter is coupled to the plurality of clocked comparators via the combiner.

31. (previously presented) A receiver, comprising: an antenna configured to receive a RF signal; a filter coupled to the antenna, the filter configured to filter the received RF signal; an amplifier coupled with the filter, the amplifier configured to amplify the filtered RF signal; and a radio receiver, comprising: a band pass filter configured to filter a combined signal; a plurality of clocked comparators coupled with the band pass filter, the clocked comparators configured to compare the filter combined signal to a ground reference when each comparator is enabled by a clock signal, and wherein each of the clocked comparators is configured to be activated on a different phase of the clock signal; a combiner coupled to the plurality of clocked comparators, the combiner configured to combine the outputs of the clocked comparators; a digital-to-analog converter coupled with the clocked comparators), the digital-to-analog converter configured to convert the output of the clocked comparators to an analog signal; and a combiner configured to receive the amplified and filtered RF signal and combine the amplified and filtered RF signal with the analog signal generated by the digital-to-analog converter in order to generate the combined signal.

32. (original) The receiver of claim 31, wherein the radio receiver further comprises filtering and decimation circuitry configured to filter and decimate the output of the clocked comparator.

33. (original) The receiver of claim 31, wherein the radio receiver further comprises a clock signal configured to clock the clocked comparator at a rate required to achieve a selected effective number of bits at the output of the filtering and decimation circuitry.

34. (original) The receiver of claim 31, wherein the combiner is a passive combiner.
(Canceled).

35. (previously presented) The receiver of claim 31, wherein the digital-to-analog converter is coupled to the plurality of clocked comparators via the combiner.

36. (canceled).

37. (canceled).